



Industrial IT, Elektronik
 Hard- Software Entwicklung
 SW-Engineering, Automation
 www.deltronic.ch

Embedded for Military
 Ruggedized PC/104+ products for
 embedded military applications.
 VersaLogic.com

Develop: Embedded Systems
 ARM XScale and Intel Atom boards
 Z510, Z530, PXA270, 300, 310,
 320
 www.toradex.com/Embedded-Comp Ads by Google

EDA News - electronic design automation, semiconductor, embedded system

home : : contact : : embedded : : fpga : : eda blog : : free journals : : jobs

[Ads by Google](#) [Process Improvement](#) [EDA](#) [Event Driven Process](#) [IC Design Manager](#) [RF Transmitter IC](#)

EDA Geek publishes news about the electronic design automation and semiconductor industry.

Ads by Google

Industrial IT, Elektronik

Hard- Software Entwicklung
 SW-Engineering, Automation
 www.deltronic.ch

Develop: Embedded Systems

ARM XScale and Intel Atom boards Z510, Z530, PXA270, 300, 310, 320
 www.toradex.com/Embedded

FPGA Cluster Computing

Massively parallel, incredibly fast
 Download a free white paper
 www.picocomputing.com

EDA Design Flow Platform

System for developing, managing and visualizing design flows.
 www.rtda.com/maximize-wor

Embedded PC/104+ SBC

AMD Geode LX 800 / Soldered DDR 5W / Fanless / Integrated Video
 VersaLogic.com

News Archives

- [Boards, Busses](#)
- [BSP](#)
- [Chassis](#)
- [Components](#)
- [Database](#)
- [Design Flow](#)
- [Design Services](#)
- [DSPs](#)
- [EDA Tools](#)
- [Embedded Systems](#)
- [Equipment](#)
- [Events, Training](#)
- [Foundry](#)
- [FPGAs](#)
- [IDE](#)
- [Industrial](#)
- [IP Cores](#)
- [Microcontrollers](#)
- [Models, Simulations](#)
- [Networking](#)
- [Other](#)
- [Reference Design](#)
- [Research](#)
- [Robotics](#)
- [RTOS](#)
- [SIP](#)
- [Software](#)
- [Test Solution](#)
- [UML](#)
- [Wireless](#)

Follow EDA Geek: [Twitter @edaGeek](#) | [RSS](#)
 Email:

AWR Collaborate on Advanced Radio Systems-on-Chip

Posted by Ken Cheung in [Wireless](#) on Tuesday, November 4, 2008
[NXP Licenses Differential DPA Patents from Cryptography Research](#) »

« [MontaVista Offers Webinars for Improving Embedded Linux Startup Time](#)

AWR, the innovation leader in high-frequency electronic design automation (EDA), announced that its Finnish R&D facility will conduct research within the European Integrated Circuit/Electromagnetic Simulation and design Technologies for Advanced Radio Systems-on-chip (ICESTARS) project. ICESTARS strives to enable the development of low-cost wireless chips that can operate at frequencies up to 100 GHz.

"With mobile device services such as Internet access, mobile TV, and remote banking becoming mainstream, very high data transfer rates are a must," said Taisto Tinttunen, R&D director of AWR-APLAC Corporation. "Consequently, there is a growing demand for simulation technologies that not only address these fast data transfer rates but also scale well beyond current frequencies used for wireless communications." AWR's efforts within the scope of ICESTARS will focus on frequency-domain simulation methods for next-generation wireless system on chip (SoC) devices, and will be led by Mr. Tinttunen.

The leader of the ICESTARS project, Marq Kole of NXP Semiconductors, said: "By the end of the project in 2010 we aim to have accelerated the chip development process in the extremely high frequency (EHF) range through use of new methods and simulation tools that will allow European chip developers to maintain a top position over the whole wireless communications spectrum."

The ICESTARS project is funded by the European Commission within its Seventh Research Framework Programme (FP7) and is led by NXP Semiconductors. The German semiconductor company Qimonda will develop advanced analog simulation techniques, and MAGWEL of Belgium will focus on electromagnetic simulation. University partners include The Mathematical Institute of the University of Cologne (Germany), Upper Austria University of Applied Sciences, the University of Wuppertal (Germany), and the University of Oulu (Finland). The universities will concentrate on modeling, algorithmic problems, and simulation issues that must be solved to produce robust and accelerated automated testing of analog circuits with digital signal processing in the EHF region.

You might also like:

- | | | | |
|--|--|--|--|
| | | | |
|--|--|--|--|

[LinkWithin](#)

- Web edageek.com
 www.embeddedstar.com edablog.com

EDA Geek Newsletter

Don't have time to visit EDA Geek everyday? Then sign up for our free newsletter. We'll send you an email when we have something to share with you. Your email address will be kept confidential and we will not share, sell, or rent it to anyone. You can unsubscribe at any time by clicking a link in the email.

Enter your email address to sign up for our free newsletter:

If you are familiar with RSS feeds, you can also sign up for our free [news feed](#). Our RSS feed is updated in real-time while our newsletter is updated daily.

Coverventor
 Virtual MEMS Processing
 Exploded view of DMD mirror
[Learn More](#)
www.coverventor.com Ads by Google

Digi-Key
NEW DIGI-KEY TOOLBAR
 DOWNLOAD THE LATEST VERSION TODAY!
 SEARCHING FOR ELECTRONIC COMPONENTS HAS NEVER BEEN EASIER.

 QUICK. DIRECT. FREE! digikey.com

FREE CLICK HERE!
 ENGINEERING PUBLICATIONS

FREE CLICK HERE!
 TECHNICAL PUBLICATIONS

EDA News

- [Essensium Receives Patent for Wide Over Narrowband RF Implementation](#)
- [Open-Silicon Acquires Silicon Logic Engineering](#)
- [IEI Technology Unveils PCIe-Q57 PICMG 1.3 Full-size CPU Card](#)
- [Freescale Semiconductor, Mentor Graphics Leverage Design Flow](#)
- [87% of Wi-Fi Enabled Smartphones to Feature 802.11n Plus Semi and X-FAB Launches Plessey Semiconductors](#)
- [Microchip Enables Wi-Fi for PIC Microcontrollers by Acquiring ZeroG](#)
- [GE Intelligent Platforms CT12 6U CompactPCI Single Board Computer](#)
- [Mindspeed Concerto 1000 Packet Processor Features TeamF1 Secure Gateway](#)
- [Honeywell Rolls Out HMC5883 3-Axis Compass Chip for Handheld Devices](#)



Embedded Blog

[CMX-RTX and CMX-TINY+ Real-Time Operating](#)